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|---------------|---|-------|------------|
| FILE NAME:    | Multiplex_var1.DSN  | DATE: | 07.10.2010 |
| DESIGN TITLE: | D:\Proejcts_freelanse\PCB_design_Elance_oyikPr                              | PAGE: | 1 of 1     |
| PATH:         | D:\Proejcts_freelanse\PCB_design_Elance_oyikPr\Proejus_files\Multiplex_var1 | REV:  | <NONE>     |
| BY:           | <NONE>  | TIME: | 13:06:17   |